

Abstract of the Disclosure:

A thyristor structure having a first terminal, formed as a first region with a first conductivity type, is provided. A second region of a second conductivity type adjoins the first
5 region. A third region of the first conductivity type, which adjoins the second region, has a common surface with the latter. A second terminal, as fourth region of the second conductivity type, adjoins the third region. At the common surface of the second region and the third region, an
10 auxiliary electrode is disposed in a manner adjoining at least one of the two regions.

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